

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Atty. Docket

DOMINICUS M.W. LEENAERTS ET AL

PHNL 010030

Serial No.:

Filed: CONCURRENTLY

Title: TURNABLE INTEGRATED RF FILTER HAVING SWITCHED FIELD
EFFECT CAPACITORS

Commissioner for Patents
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Sir:

Prior to calculation of the filing fee and examination,
please amend the above-identified application as follows:

IN THE CLAIMS

Please amend the claims as follows:

3. (Amended) The filter (3) according to claim 1,
characterised in that the filter (3) is provided with control
means (Contr.) coupled to FET capacitor control inputs (G1-
32; G'1-32).

4. (Amended) The filter (3) according to claim 1,
characterised in that the FET capacitors (M1-32; M'1-32) are
split in equally controlled pairs of FET capacitors (M1-32;
M'1-32).

5. (Amended) The filter (3) according to claim 1,
characterised in that the filter (3) is built up as a

symmetrically filter (3) having a symmetrical input (5) and a symmetrical output (6).

6. (Amended) The filter (3) according to claim 1, characterised in that two or more of the FET capacitors (M1-32; M'1-32) are connected in series.

7. (Amended) The filter (3) according to claim 1, characterised in that the FET capacitors (M1-32; M'1-32) are metal oxide semiconductor (MOSFET) capacitors (M1-32; M'1-32).

8. (Amended) A transmitter, receiver, or transceiver having a filter (3) according to claim 1, which filter (3) is provided with field effect (FET) capacitors (M1-32; M'1-32) arranged for controlling their respective capacity values, each such FET capacitor (M1-32; M'1-32) having a source (S) and a drain (D), characterised in that the source (S) and the drain (D) of each FET capacitor (M1-32; M'1-32) are coupled to one another.

REMARKS

The foregoing Preliminary Amendment to claims 3-8 was made solely to avoid filing the claims in the multiple dependant form so as to avoid the additional filing fee.

The claims were not amended in order to address issues of patentability and Applicant respectfully reserves all rights she may have under the Doctrine of Equivalents. Applicant furthermore reserves her right to reintroduce

subject matter deleted herein at a later time during the
prosecution of this application or continuing applications.

Respectfully submitted,

By 

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APPENDIX

3. (Amended) The filter (3) according to claim 1 ~~or 2~~, characterised in that the filter (3) is provided with control means (Contr.) coupled to FET capacitor control inputs (G1-32; G'1-32).

4. (Amended) The filter (3) according to ~~one of the claims~~ 1-3, characterised in that the FET capacitors (M1-32; M'1-32) are split in equally controlled pairs of FET capacitors (M1-32; M'1-32).

5. (Amended) The filter (3) according to ~~one of the claims~~ 1-4, characterised in that the filter (3) is built up as a symmetrically filter (3) having a symmetrical input (5) and a symmetrical output (6).

6. (Amended) The filter (3) according to ~~one of the claims~~ 1-5, characterised in that two or more of the FET capacitors (M1-32; M'1-32) are connected in series.

7. (Amended) The filter (3) according to ~~one of the claims~~ 1-6, characterised in that the FET capacitors (M1-32; M'1-32) are metal oxide semiconductor (MOSFET) capacitors (M1-32; M'1-32).

8. (Amended) A transmitter, receiver, or transceiver having a filter (3) according to ~~one of the claims~~ 1-7, which filter (3) is provided with field effect (FET) capacitors (M1-32; M'1-32) arranged for controlling their respective capacity values, each such FET capacitor (M1-32; M'1-32) having a source (S) and a drain (D), characterised in that the source (S) and the drain (D) of each FET capacitor (M1-32; M'1-32) are coupled to one another.